

(19) World Intellectual Property
Organization
International Bureau



(43) International Publication Date
29 July 2004 (29.07.2004)

PCT

(10) International Publication Number
WO 2004/063913 A1

(51) International Patent Classification⁷: **G06F 1/12**,
1/14, G04G 5/00

Tecnologia, Av. Fernando Ferrare, 845, Campus Univer-
sitária Alaor Queiroz, ES-29060-410 Araújo-Goiaberas
(ES).

(21) International Application Number:
PCT/BR2003/000130

(22) International Filing Date:
16 September 2003 (16.09.2003)

(25) Filing Language: English

(26) Publication Language: English

(30) Priority Data:
PI 0300100-8 10 January 2003 (10.01.2003) BR

(72) Inventors; and

(75) Inventors/Applicants (for US only): **AMORIM, Clau-
dio, Luis** [BR/BR]; Rua Humaitá, 104, Apto. 1006,
Botafogo, CEP-22261-001 Rio de Janeiro, RJ (BR). **DE
SOUZA FERREIRA, Alberto** [BR/BR]; Av. Dante
Micheline, 2431 Apto 803, Mata da Praia, ES-29066-430
Vitoria (ES).

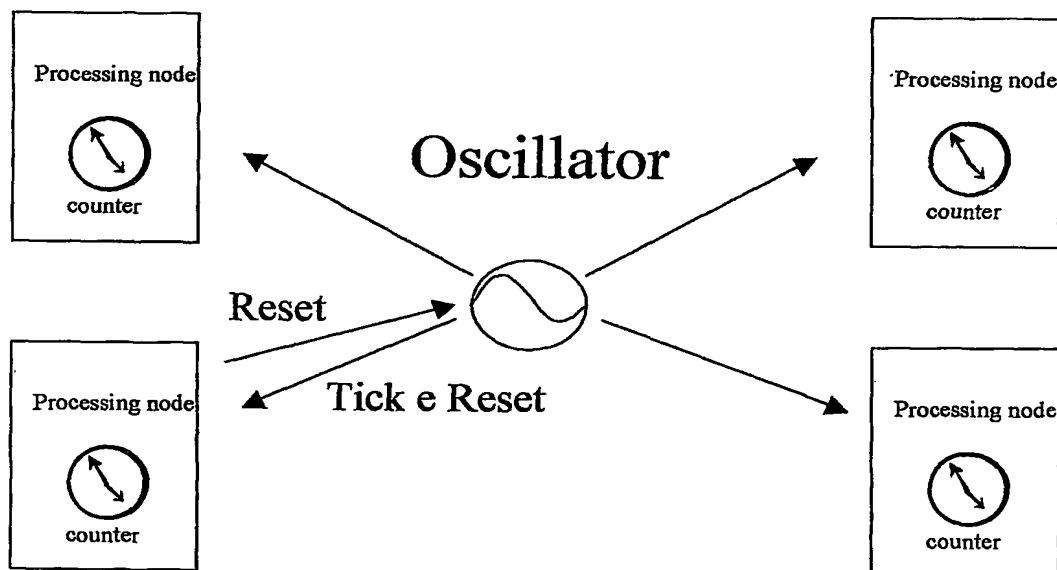
(74) Agent: **JOUBERT GONCALVES, Castro**; Praia de
Icaraí, 237/1302, CEP-24230-003 Niteroi-RJ (BR).

(71) Applicants (for all designated States except US):
**COPPE/UF RJ - COORDENAÇÃO DOS PROGRA-
MAS DE PÓS GRADUAÇÃO DE ENGENHARIA DA
UNIVERSIDADE FEDERAL DO RIO DE JANEIRO**
[BR/BR]; Centro de Tecnologia, s/nº, Bloco G, Cidade
Universitária, CEP-21945-970 Ilha do Fundão, RJ (BR).
FEST-FUNDACÃO ESPÍRITO [BR/BR]; Santense de

(81) Designated States (national): AE, AG, AL, AM, AT, AU,
AZ, BA, BB, BG, BY, BZ, CA, CH, CN, CO, CR, CU, CZ,
DE, DK, DM, DZ, EC, EE, ES, FI, GB, GD, GE, GH, GM,
HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK,
LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX,
MZ, NO, NZ, OM, PH, PL, PT, RO, RU, SC, SD, SE, SG,
SK, SL, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC,
VN, YU, ZA, ZM, ZW.

[Continued on next page]

(54) Title: DISTRIBUTED GLOBAL CLOCK FOR CLUSTERS OF COMPUTERS



(57) Abstract: The present invention refers to a global clock system for clusters or networks of computers implemented entirely in hardware. The system uses a specifically designed hierarchical network to distribute clock pulses that are used to increment time counters in the cluster' nodes. In addition, this network enables any node of the cluster to send a reset signal to the other nodes so that all local time counters are initialized simultaneously and remain automatically synchronized afterwards. In this way, each processor in the cluster is able to obtain the value of the global clock whenever accessing its own local time counter. The reset signal is the only function that is implemented in software.



(84) **Designated States (regional):** ARIPO patent (GH, GM, KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IT, LU, MC, NL, PT, RO, SE, SI, SK, TR), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

Published:

— with international search report

For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.